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CIRCUIT AND SYSTEM FOR ACCESSING MEMORY MODULES

ABSTRACT OF THE DISCLOSURE

A circuit and system for improving signal integrity in a memory system. The circuit has a transmission line having a dampening impedance between a driver and a
5 branch point of the transmission line. The circuit also has a termination impedance having one end coupled to the transmission line between the dampening impedance and the branch point. The transmission line has branches coupled to memory module interfaces. The branches have respective lengths between the branch point and the
10 memory module interfaces to be configured symmetrically, wherein the branch point is at a point to balance signal transmission on the branches.